

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT

In re Patent Application of: Ji Suk HONG, et al.

Serial No.: New Application Group Art Unit: Not Yet Assigned

Filed: March 18, 2002

Examiner: Not Yet Assigned

Title: METHOD FOR FABRICATING SEMICONDUCTOR MEMORY DEVICE

INFORMATION DISCLOSURE STATEMENT

Honorable Commissioner for Patents
Washington, DC 20231

March 18, 2002

Sir:

As a means of complying with the duty of disclosure under 37 CFR §1.56, and in accordance with 37 CFR §§1.97 and 1.98, Applicant(s), through the undersigned attorney, submits this Information Disclosure Statement. The patents, publications or other information submitted herewith are listed on the attached Form PTO-1449 and copies are attached.

In accordance with 37 CFR §1.97(b)(1) or (2), this Information Disclosure Statement is being filed either within three months of the filing date of the above-identified application, or within three months of the date of entry into the national stage of the above-identified application as set forth in 37 CFR §1.491. Accordingly, no fee is required.

Respectfully submitted,

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